



Certificate

This is to certify that the thesis entitled '*Design and Implementation of DSP Based Control of Hybrid Cascaded Multilevel Inverter Using Various Novel Pulse Width Modulation Techniques*' submitted by *Mrs. Meeta K Matnani* in fulfillment of the degree of **DOCTOR OF PHILOSOPHY** in *Electrical Engineering Department*, Faculty of Technology & Engineering, The Maharaja Sayajirao University of Baroda, Vadodara is a bonafide record of investigations carried out by her in the Department of Electrical Engineering, Faculty of Technology & Engineering, The Maharaja Sayajirao University of Baroda, Vadodara under my guidance and supervision. In my opinion the standards fulfilling the requirements of the Ph.D. Degree as prescribed in the regulations of the University has been attained.

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