



## ***Declaration***

I, **Mrs. Meeta K. Matnani** hereby declare that the work reported in this thesis entitled '**Design And Implementation of DSP Based Control of Hybrid Cascaded Multilevel Inverter Using Various Novel Pulse Width Modulation Techniques**' submitted for the award of the degree of **DOCTOR OF PHILOSOPHY** in Electrical Engineering Department, Faculty of Technology & Engineering, The Maharaja Sayajirao University of Baroda, Vadodara is original and has been carried out in the Department of Electrical Engineering, Faculty of Technology & Engineering, The Maharaja Sayajirao University of Baroda, Vadodara. I further declare that this thesis is not substantially the same as one, which has already been submitted in part or in full for the award of any degree or academic qualification of this University or any other Institution or examining body in India or abroad.

**February 2014**

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