

Certificate

This is to certify that the thesis entitled, "Design and Implementation of Embedded Architecture Using Soft-Computing Techniques for Parametric Optimization of MIMO Wireless System" submitted by Pooja S. Suratia in fulfillment of the degree of DOCTOR OF PHILOSOPHY in Electrical Engineering Department, Faculty of Technology and Engineering, The Maharaja Sayajirao University of Baroda. Vadodara is a bonafide record of investigations carried out by her under my guidance and supervision. In my opinion the standards fulfilling the requirements of the Ph.D. Degree as prescribed in the regulations of the University has been attained.

Prof. Satish K. Shah Guide, Electrical Engineering Department,

Faculty of Technology and Engineering, The Maharaja Sayajirao University of Baroda, Vadodara.

Dr. Satish K. Joshi

Head, Electrical Engineering Department, Faculty of Technology and Engineering, The Maharaja Sayajirao University of Baroda, Vadodara.

Mu

Faculty of Technology and Engineering, The Maharaja Sayajirao University of Baroda, Vadodara.