

I, Pooja S. Suratia hereby declare that the work reported in this thesis entitled, "Design and Implementation of Embedded Architecture Using Soft-Computing Techniques for Parametric Optimization of MIMO Wireless System" submitted for the degree of DOCTOR OF PHILOSOPHY in Electrical Engineering Department, Faculty of Technology and Engineering, The Maharaja Sayajirao University of Baroda, Vadodara, is original and has been carried out in the Electrical Engineering Department, Faculty of Technology and Engineering, The Maharaja Sayajirao University of Baroda, Vadodara. I further declare that this thesis is not substantially the same as one, which has already been submitted in part or in full for the award of any degree or academic qualification of this University or any other Institution or examining body in India or abroad.

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Pooja S. Suratia